

United States Patent and Trademark Office

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

CONFIRMATION NO. FIRST NAMED INVENTOR ATTORNEY DOCKET NO. APPLICATION NO. FILING DATE 03/24/1999 MARK WILLIAM JANOSKA 1400.4100209 1410 09/275,934 25697 12/10/2003 **EXAMINER** 7590 ROSS D. SNYDER & ASSOCIATES, INC. HOANG, THAI D 115 WILD BASIN RD. ART UNIT PAPER NUMBER **SUITE 107** AUSTIN, TX 78746 2667 DATE MAILED: 12/10/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

			A 1' (/-)		
Office Action Summary	Application	n No.	Applicant(s)		
	09/275,934	1	JANOSKA ET AL.		
	Examiner		Art Unit		
	Thai D Hoa	-	2667		
The MAILING DATE of this communication appeared for Reply	ppears on the	cover sheet with the co	orrespondence address		
A SHORTENED STATUTORY PERIOD FOR REP THE MAILING DATE OF THIS COMMUNICATION - Extensions of time may be available under the provisions of 37 CFR 1 after SIX (6) MONTHS from the mailing date of this communication If the period for reply specified above is less than thirty (30) days, a re - If NO period for reply is specified above, the maximum statutory perio - Failure to reply within the set or extended period for reply will, by statu - Any reply received by the Office later than three months after the mail earned patent term adjustment. See 37 CFR 1.704(b). Status	1. 1.136(a). In no ever eply within the statut od will apply and will ute, cause the applic	nt, however, may a reply be tim fory minimum of thirty (30) days expire SIX (6) MONTHS from to cation to become ABANDONED	ely filed will be considered timely. the mailing date of this communica () (35 U.S.C. § 133).	ation.	
1) Responsive to communication(s) filed on 17	November 20	<u>03</u> .			
2a) This action is FINAL . 2b) ⊠ Thi	is action is no	n-final.			
) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.				
Disposition of Claims					
4) Claim(s) 1-27 is/are pending in the application	4)⊠ Claim(s) <u>1-27</u> is/are pending in the application.				
4a) Of the above claim(s) is/are withdr	4a) Of the above claim(s) is/are withdrawn from consideration.				
5)⊠ Claim(s) <u>24-27</u> is/are allowed.					
6)⊠ Claim(s) <u>1-5,7-9 and 11-21</u> is/are rejected.					
7)⊠ Claim(s) <u>6,10,22 and 23</u> is/are objected to.					
8) Claim(s) are subject to restriction and	or election re	quirement.			
Application Papers					
9) The specification is objected to by the Examin	ner.				
10) The drawing(s) filed on is/are: a) □ accepted or b) □ objected to by the Examiner.					
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).					
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).					
11)☐ The oath or declaration is objected to by the l	Examiner. Not	te the attached Office	Action or form PTO-152	2.	
Priority under 35 U.S.C. §§ 119 and 120					
12) Acknowledgment is made of a claim for forei a) All b) Some * c) None of: 1. Certified copies of the priority docume 2. Certified copies of the priority docume 3. Copies of the certified copies of the pr application from the International Bure * See the attached detailed Office action for a li 13) Acknowledgment is made of a claim for domes since a specific reference was included in the fi 37 CFR 1.78. a) The translation of the foreign language p 14) Acknowledgment is made of a claim for domes reference was included in the first sentence of	ents have been this have been this have been this have been the current of the certification priority unfirst sentence provisional appartic priority un	n received. In received in Application received in Application its have been received a 17.2(a)). Ited copies not received der 35 U.S.C. § 119(e) of the specification or oblication has been received der 35 U.S.C. §§ 120	on No Id in this National Stage d. e) (to a provisional application Data Stage eived. and/or 121 since a spec	cation) Sheet. cific	
Attachment(s)		A) D Intonders Comme	(DTO 442) Danca No.(-)		
 Notice of References Cited (PTO-892) Notice of Draftsperson's Patent Drawing Review (PTO-948) Information Disclosure Statement(s) (PTO-1449) Paper No(s) 			(PTO-413) Paper No(s) atent Application (PTO-152)		

U.S. Patent and Trademark Office PTOL-326 (Rev. 11-03) Application/Control Number: 09/275,934

Art Unit: 2667

DETAILED ACTION

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 1-5, 7-9, 11-21 are rejected under 35 U.S.C. 103(a) as being unpatentable over Sakamoto, U.S patent No. 6,075,767.

Regarding claims 1 and 15, Sakamoto discloses a system having a redundant architecture for switchover to a line interface. Sakamoto discloses that the system comprises a switch core (2), wherein the switch core has a plurality of inputs and a plurality of outputs, wherein the switch core passes data received on the plurality of inputs to the plurality of outputs based on routing tags (col. 1, lines 13-17; col. 2, lines 19-22; col. 9, lines 8-11; col.13, lines 4-6); and a plurality of line card managers (3) operably coupled to the switch core (2) and adapted to couple to a plurality of line card pairs (1-1 and 1-2), wherein each line card manager includes an arbiter (9) that couples to a first line card and a second line card of a line card pair, wherein each line card manager couples to a different line card pair, wherein each arbiter is operably coupled to a corresponding input of the plurality of inputs of the switch core, wherein the arbiter provides ingress data from one of the first and second line cards to the corresponding input to the switch core based on selection information (figures 1 and 17; col. 7, line 40 -

col. 8, line 67.) Sakamoto does not explicitly disclose that the line card manager (3) includes a router. However, Sakamoto discloses that the first and second line cards (1-1 and 1-2) comprise a routing function (col. 2, lines 20-22; col. 9, lines 8-11; col. 12, lines 50-55; col.13, lines 4-6), which provides egress data from the corresponding output to the first and second line cards based on routing information included in the egress data (fig. 3, col. 2, lines 23-25). It would have been obvious to one of ordinary skill in the art at the time the invention was made to modify the routing function in the first and second line cards disclosed by Sakamoto into the line card manager in order to simplify the structure and reduce the cost of the system.

Regarding claims 2, 3, 16 and 19, Sakamoto does not disclose that each line card manager further comprises buffering circuitry operably coupled to the arbiter, wherein the buffering circuitry buffers ingress data from the first and second line cards, wherein the arbiter provides ingress data from the buffering circuitry to the switch core based on the selection information. However, buffers are used in most of telecommunications systems. It would have been obvious to one of ordinary skill in the art at the time the invention was made to add a buffer into the system disclosed by Sakamoto in order to control data flow in the system.

Regarding claims 4 and 17, Sakamoto discloses that the selection information determines an active line card and an inactive line card of the line card pair, wherein the arbiter preferentially passes active line card data over inactive line card data (col. 8, lines 28-67.)

Application/Control Number: 09/275,934

Art Unit: 2667

Regarding claims 5 and 21, Sakamoto discloses that the redundant line card becomes active line card when a defect or failure is detected in the first line card (col. 1, lines 48-52; col. 2, lines 27-33; col. 7, line 62 – col. 8, line 8.) inactive line card data is selected when idle data in active line card is detected.

Regarding claims 7 and 20, the system disclosed by Sakamoto inherently comprises filters operably coupled to the arbiter (9), wherein the filters pass selected data types and reject other data types in order to select useful signals (col. 8, lines 28-33, and 53-59.)

Regarding claim 8, Sakamoto discloses that the register (27) that determines the selected data types.

Regarding claim 9, the register (27) in the line card manager (3) of the system disclosed by Sakamoto inherently comprises active register and an inactive register, wherein the active register configures a filter corresponding to the active line card, and the inactive register configures a filter corresponding to the inactive line card (figures 1 and 7; col. 7, line 62 – col. 8, line 8.)

Regarding claim 11, the system disclosed by Sakamoto comprises a NxN switch core and the plurality of line cards includes 2N line cards (figures 1-4 and 17.)

Regarding claims 12-14, Sakamoto discloses that the system comprises an ATM switch; therefore, it is used in a cell based network.

Regarding claim 18, Sakamoto discloses a system having a redundant architecture for switchover to a line interface (figure 1). Sakamoto discloses that the system comprises the steps of selecting ingress data from data received from a first line

card and a second line card, wherein selecting is based on an active select signal, wherein the active select signal determines an active line card and an inactive line card from the first and second line cards; providing the ingress data to an input of a switch core, wherein the switch core includes a plurality of inputs and a plurality of outputs; receiving egress data from one of the plurality of outputs of the switch core; and selectively providing the output data to at least one of the first and second line cards based on routing information included in the egress data (figures 1,12 and 17; col. 7. line 40 - col. 8, line 67.) Sakamoto does not explicitly disclose that the line card manager (3) includes a router. However, Sakamoto discloses that the first and second line cards (1-1 and 1-2) comprise a routing function (col. 2, lines 20-22; col. 9, lines 8-11; col. 12, lines 50-55; col.13, lines 4-6), which provides egress data from the corresponding output to the first and second line cards based on routing information included in the egress data (fig. 3, col. 2, lines 23-25). It would have been obvious to one of ordinary skill in the art at the time the invention was made to modify the routing function in the first and second line cards disclosed by Sakamoto into the line card manager in order to simplify the structure and reduce the cost of the system.

Allowable Subject Matter

Claims 6, 10 and 22-23 objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Claims 24-27 are allowed.

Application/Control Number: 09/275,934 Page 6

Art Unit: 2667

The following is a statement of reasons for the indication of allowable subject matter:

Sakamoto et al., US Patent No. 6,075,767 disclose an ATM handler.

Each independent claim of the present application discloses the following features:

A method for managing line cards in a switch that includes redundant line cards, comprising: a first line card and a second line card, wherein the switch provides egress data to at least one of the first line card and the second line card based on a routing information included in the egress data; wherein the routing information included in the egress data further comprises a first bit and a second bit, wherein when the first bit is active the egress data is provided to the active line card, and wherein when the second bit is active, the egress data is provided to the inactive line card as recited in claims 24 and 27; or

wherein the routing information included in the egress data further comprises a

first bit and a second bit, wherein when the first bit is active the egress data is provided

to the first line card, and wherein when the second bit is active, the egress data is

provided to the second line card as recited in claims 25 and 26.

Sakamoto et al. do not teach or fairly suggest the features as shown above.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thai D Hoang whose telephone number is (703) 305-3232. The examiner can normally be reached on Monday-Friday 8:30am-5:00pm.

Application/Control Number: 09/275,934

Art Unit: 2667

Page 7

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Chi Pham can be reached on (703) 305-4378. The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9314.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 305-4700.

Thai Hoang

CHI PHAM

SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2600 12/8/63